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L4: Entry 2 of 21

File: USPT

Apr 20, 2004

DOCUMENT-IDENTIFIER: US 6725372 B1

TITLE: Digital watermarking

Detailed Description Text (10):

It will be appreciated that a number of techniques are known for implementing the MPEG encoder 10 and the MPEG decoder 100 described above. All of the functions, except for analog/digital and digital/analog conversion of the video signal, may be accomplished with software executing on a microprocessor in a computer, or on a microcontroller or programmable digital signal processor in an embedded system. The system may be integrated in its entirety into an application-specific integrated circuit, programmable gate array, programmable logic device, or other system-on-a-chip design. Additionally, a number of video processing chips and chip sets are commercially available that are pre-configured to code and decode MPEG-2 media streams. Any of these devices may be adapted to operate according to the teachings of the invention.

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L3: Entry 2 of 4

File: USPT

Apr 2, 2002

DOCUMENT-IDENTIFIER: US 6366771 B1

TITLE: Wireless communication network having voice and data communication capability

Detailed Description Text (4):

Referring to FIG. 2, each access point 12, is coupled to the backbone network 20 via a standard network connector. A processor 30 generally controls operation of the access point 12. In the preferred embodiment, an ISA bus interconnects the processor 30 LAN communication circuitry 33 and RF communication circuitry 38, however, other bus structures could be used. The LAN communication circuitry 33 is preferably a commonly available ISA peripheral Ethernet or TokenRing chip set. An appropriate software driver for interchanging data between the processor 30 and the LAN communication circuitry 33 is loaded into memory 43 and executed by processor 30. In the preferred embodiment, the RF communication circuitry 38 includes a commonly available ISA peripheral PCMCIA controller 48 and a PCMCIA data radio 50 operating in accordance with the IEEE 802.11 RF communication protocol. An antenna 54 connects to the radio 50 and propagates the transmitted signal for communicated data from the radio 50 to mobile RF devices 65 and captures propagated signals transmitted by the mobile RF devices 65 (FIG. 1).

Detailed Description Text (8):

The gateway 25 facilitates pseudo full duplex communication between portable network telephones 100 and between a portable network telephone 100 and a standard analog or digital telephone coupled to the gateway 25 via a proprietary telephone network 157 or a subscriber loop 158 to a public network (PBX) 140. The gateway 25 is shown in more detail in FIG. 3. A processor 160 generally controls operation of the gateway 25. In the preferred embodiment, an ISA bus 16 interconnects the processor 290 with LAN communication circuitry 170 and PBX interface circuitry 180, however, other bus structures could be used. Like the access points 12, the LAN communication circuitry 170 is preferably a commonly available ISA peripheral Ethernet or TokenRing chip set and an appropriate software driver for interchanging data between the processor 160 and the LAN communication circuitry 170 is loaded into memory 183 and executed by processor 160. The PBX interface circuitry 180 includes digital and analog interfaces 182, 183, respectively, and operates to send and receive telephone calls over the proprietary digital telephone network 182 and over the subscriber loop to the PBX 158. Circuitry for interfacing and communicating with the proprietary digital telephone network and PBX is commonly available from companies such as AT&T and Ericsson. Again, an appropriate software driver for communicating with the PBX interface circuitry 180 is loaded into the memory 185 and operated by the processor 160.

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